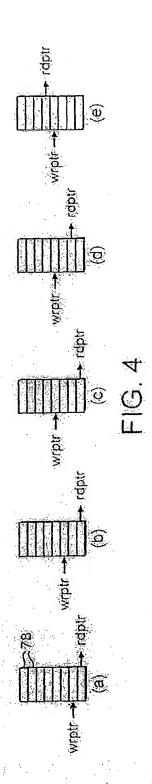


Title: BRIDGE CIRCUIT FOR USE IN RETIMING IN A SEMICONDUCTOR INTEGRATED CIRCUIT Inventor: Paul Elliott Express Mail No. EV336611152US Docket No. 851963.413

417



WRITE POINTER	O	0	0	-	2	63	5		2 9	0	-	ΩÌ.	C	Č)	ന	ത	က	ന	დ.	(7)	ന	<u>ო</u>	(C)	m	ന
READ POINTER	Ó	o	-	0		2	100	6	3	63	60	3	ന	c	ო	4	က	Ó	2	0	-	24	c	3	က
WORD COUNT	0	0	0	-	-	-	-	2	ω 4		3	~	ω	00	60,	~	ဖ	က	4	co.	C)	-	0	0	0
FIFO STATUS	E	MPTY	\ \											FU									Ē	YTGN	>

FIG. 6

